HEF4538B

Dual precision monostable multivibrator

Rev. 11 — 19 October 2018

Product data sheet

1. General description

The HEF4538B is a dual retriggerable-resettable monostable multivibrator. Each multivibrator has an active LOW trigger/retrigger input ($n\overline{A}$), an active HIGH trigger/retrigger input (nB), an overriding active LOW direct reset input ($n\overline{CD}$), an output (nQ) and its complement ($n\overline{Q}$), and two pins (nREXT/CEXT, and nCEXT, always connected to ground) for connecting the external timing components C_{EXT} and R_{EXT} . Typical pulse width variation over the specified temperature range is ± 0.2 %.

The multivibrator may be triggered by either the positive or the negative edges of the input pulse and will produce an accurate output pulse with a pulse width range of 10 μ s to infinity. The duration and accuracy of the output pulse are determined by the external timing components C_{EXT} and R_{EXT} . The output pulse width (t_W) is equal to $R_{EXT} \times C_{EXT}$. The linear design techniques in LOCMOS (Local Oxide CMOS) guarantee precise control of the output pulse width. A LOW level at $n\overline{CD}$ terminates the output pulse immediately. The trigger inputs' Schmitt trigger action makes the circuit highly tolerant of slower rise and fall times.

It operates over a recommended V_{DD} power supply range of 3 V to 15 V referenced to V_{SS} (usually ground). Unused inputs must be connected to V_{DD} , V_{SS} , or another input.

2. Features and benefits

- · Tolerant of slow trigger rise and fall times
- · Fully static operation
- 5 V, 10 V, and 15 V parametric ratings
- Standardized symmetrical output characteristics
- Specified from -40 °C to +85 °C and -40 °C to +125 °C
- Complies with JEDEC standard JESD 13-B

3. Ordering information

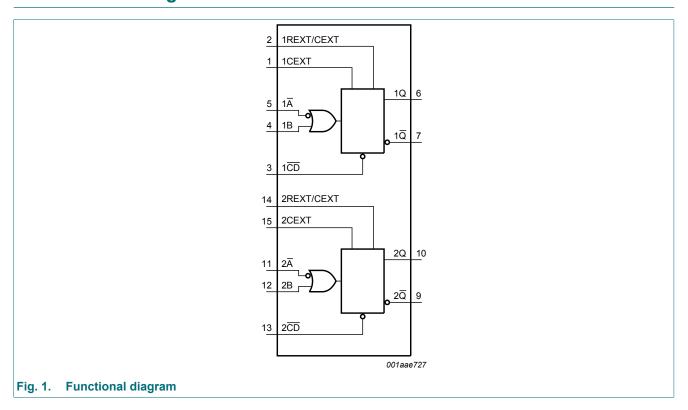
Table 1. Ordering information

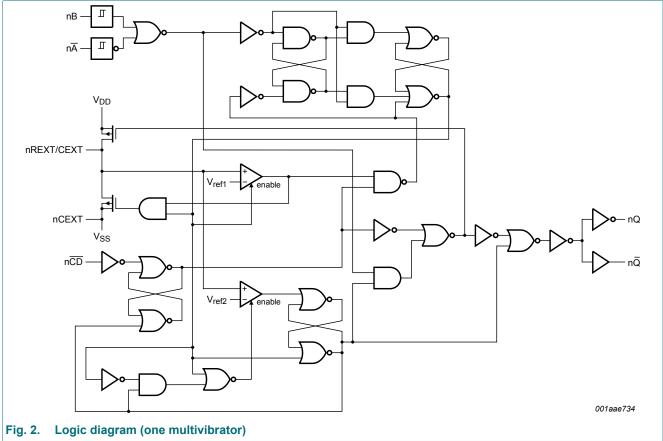
Type number	Package							
	Temperature range	Name	Description	Version				
HEF4538BT	-40 °C to +125 °C	SO16	plastic small outline package; 16 leads; body width 3.9 mm	SOT109-1				



Dual precision monostable multivibrator

4. Functional diagram

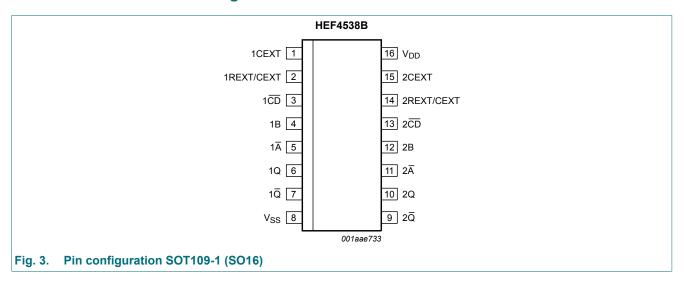




Dual precision monostable multivibrator

5. Pinning information

5.1. Pinning



5.2. Pin description

Table 2. Pin description

Symbol	Pin	Description
1CEXT, 2CEXT	1, 15	external capacitor connection (always connected to ground)
1REXT/CEXT, 2REXT/CEXT	2, 14	external capacitor/resistor connection
1CD, 2CD	3, 13	direct reset input (active LOW)
1B, 2B	4, 12	input (LOW-to-HIGH triggered)
1 A , 2 A	5, 11	input (HIGH-to-LOW triggered)
1Q, 2Q	6, 10	output
1 Q , 2 Q	7, 9	complementary output (active LOW)
V _{SS}	8	ground supply voltage
V_{DD}	16	supply voltage

6. Functional description

Table 3. Function table

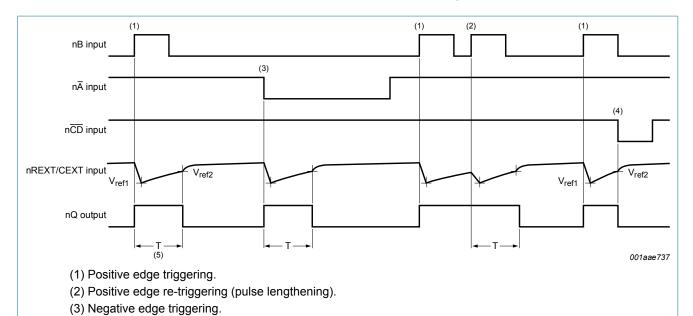
 $H = HIGH \ voltage \ level; \ L = LOW \ voltage \ level; \ X = don't \ care; \ \uparrow = positive-going \ transition; \ \downarrow = negative-going \ transition;$

 Π = one HIGH level output pulse, with the pulse width determined by C_{EXT} and R_{EXT} ;

 \coprod = one LOW level output pulse, with the pulse width determined by C_{EXT} and R_{EXT} .

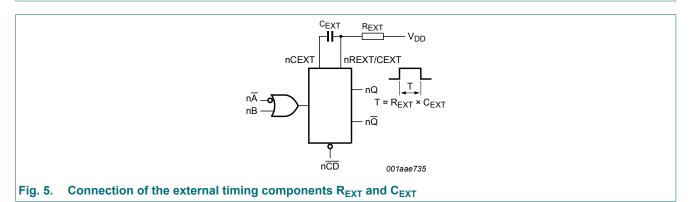
			Outputs		
nĀ	nB	nCD	nQ	nQ	
\	L	Н	Л	丁	
Н	↑	Н	Л	T.	
X	Х	L	L	Н	

Dual precision monostable multivibrator



- (4) Reset (pulse shortening).
 - (5) $T = R_{EXT} \times C_{EXT}$.

Fig. 4. **Timing diagram**



7. Limiting values

Table 4. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134). Voltages are referenced to $V_{SS} = 0 \text{ V}$ (ground)

Symbol	Parameter	Conditions	Min	Max	Unit
V_{DD}	supply voltage		-0.5	+18	V
I _{IK}	input clamping current	$V_{I} < -0.5 \text{ V or } V_{I} > V_{DD} + 0.5 \text{ V}$	-	±10	mA
VI	input voltage		-0.5	V _{DD} + 0.5	V
I _{OK}	output clamping current	$V_{I} < -0.5 \text{ V or } V_{I} > V_{DD} + 0.5 \text{ V}$	-	±10	mA
I _{I/O}	input/output current		-	±10	mA
I _{DD}	supply current		-	50	mA
T _{stg}	storage temperature		-65	+150	°C
T _{amb}	ambient temperature		-40	+125	°C
P _{tot}	total power dissipation	T_{amb} = -40 °C to +125 °C [1]	-	500	mW
Р	power dissipation	per output	-	100	mW

[1] For SO16 package: P_{tot} derates linearly with 8 mW/K above 70 °C.

HEF4538B

Product data sheet

4/14

Dual precision monostable multivibrator

8. Recommended operating conditions

Table 5. Recommended operating conditions

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V_{DD}	supply voltage		3	-	15	V
VI	input voltage		0	-	V_{DD}	V
T _{amb}	ambient temperature	in free air	-40	-	+125	°C
Δt/ΔV	input transition rise and fall rate	V _{DD} = 5 V	-	-	3.75	μs/V
		V _{DD} = 10 V	-	-	0.5	μs/V
		V _{DD} = 15 V	-	-	0.08	μs/V

9. Static characteristics

Table 6. Static characteristics

 $V_{SS} = 0 \ V$; $V_I = V_{SS} \ or \ V_{DD} \ unless \ otherwise \ specified.$

Symbol	Parameter	Conditions	V _{DD}	T _{amb} =	-40 °C	T _{amb} =	25 °C	T _{amb} =	85 °C	T _{amb} =	125 °C	Unit	
				Min	Max	Min	Max	Min	Max	Min	Max		
V _{IH}	HIGH-level	I _O < 1 μΑ	5 V	3.5	-	3.5	-	3.5	-	3.5	-	V	
	input voltage		10 V	7.0	-	7.0	-	7.0	-	7.0	-	V	
			15 V	11.0	-	11.0	-	11.0	-	11.0	-	V	
V _{IL}	LOW-level	I _O < 1 μA	5 V	-	1.5	-	1.5	-	1.5	-	1.5	V	
	input voltage		10 V	-	3.0	-	3.0	-	3.0	-	3.0	V	
			15 V	-	4.0	-	4.0	-	4.0	-	4.0	V	
V _{OH}	HIGH-level	I _O < 1 μA	5 V	4.95	-	4.95	-	4.95	-	4.95	-	V	
	output voltage	output voltage		10 V	9.95	-	9.95	-	9.95	-	9.95	-	V
			15 V	14.95	-	14.95	-	14.95	-	14.95	-	V	
V_{OL}	LOW-level	I _O < 1 μΑ	5 V	-	0.05	-	0.05	-	0.05	-	0.05	V	
	output voltage	ge	10 V	-	0.05	-	0.05	-	0.05	-	0.05	V	
			15 V	-	0.05	-	0.05	-	0.05	-	0.05	V	
I _{OH}	HIGH-level	V _O = 2.5 V	5 V	-	-1.7	-	-1.4	-	-1.1	-	-1.1	mA	
	output current	V _O = 4.6 V	5 V	-	-0.64	-	-0.5	-	-0.36	-	-0.36	mA	
		V _O = 9.5 V	10 V	-	-1.6	-	-1.3	-	-0.9	-	-0.9	mA	
		V _O = 13.5 V	15 V	-	-4.2	-	-3.4	-	-2.4	-	-2.4	mA	
I _{OL}	LOW-level	V _O = 0.4 V	5 V	0.64	-	0.5	-	0.36	-	0.36	-	mA	
	output current	V _O = 0.5 V	10 V	1.6	-	1.3	-	0.9	-	0.9	-	mA	
		V _O = 1.5 V	15 V	4.2	-	3.4	-	2.4	-	2.4	-	mA	
I _I	input leakage	nĀ, nB	15 V	-	±0.1	-	±0.1	-	±1.0	-	±1.0	μA	
	current	nREXT/CEXT	15 V	-	±0.3	-	±0.1	-	±1.0	-	±1.0	μΑ	
Cı	input capacitance		-	-	-	-	7.5	-	-	-	-	pF	

5/14

Dual precision monostable multivibrator

Table 7. Typical static characteristics

 $V_{SS} = 0 \ V; \ V_I = V_{SS} \ or \ V_{DD}; \ T_{amb} = +25 \ ^{\circ}C.$

Symbol	Parameter	Conditions	V_{DD}	Тур	Unit
I _{DD}	supply current	active state	5 V [1]	55	μΑ
			10 V	150	μA
			15 V	220	μA
Cı	input capacitance	nREXT/CEXT	-	15	pF

^[1] Only one monostable is switching: for the specified current during the output pulse (output nQ is HIGH).

10. Dynamic characteristics

Table 8. Dynamic characteristics

 V_{SS} = 0 V; T_{amb} = 25 °C; for test circuit see Fig. 11.

Symbol	Parameter	Conditions	V _{DD}	Extrapolation formula[1]	Min	Тур	Max	Unit
t _{PHL}	HIGH to LOW	$n\overline{A}$, nB to $n\overline{Q}$; see Fig. 6	5 V	193 ns + (0.55 ns/pF) C _L	-	220	440	ns
	propagation delay		10 V	74 ns + (0.23 ns/pF) C _L	-	85	190	ns
	delay		15 V	52 ns + (0.16 ns/pF) C _L	-	60	120	ns
		nCD to nQ; see Fig. 6	5 V	98 ns + (0.55 ns/pF) C _L	-	125	250	ns
			10 V	44 ns + (0.23 ns/pF) C _L	-	55	110	ns
			15 V	32 ns + (0.16 ns/pF) C _L	-	40	80	ns
t _{PLH}	LOW to HIGH	pagation		173 ns + (0.55 ns/pF) C _L	-	200	460	ns
	propagation delay		10 V	79 ns + (0.23 ns/pF) C _L	-	90	180	ns
			15 V	52 ns + (0.16 ns/pF) C _L	-	60	120	ns
		nCD to nQ; see Fig. 6	5 V	98 ns + (0.55 ns/pF) C _L	-	125	250	ns
			10 V	44 ns + (0.23 ns/pF) C _L	-	55	110	ns
			15 V	32 ns + (0.16 ns/pF) C _L	-	40	80	ns
t _t	transition time	see Fig. 6	5 V [2]	10 ns + (1.00 ns/pF) C _L	-	60	120	ns
			10 V	9 ns + (0.42 ns/pF) C _L	-	30	60	ns
			15 V	6 ns + (0.28 ns/pF) C _L	-	20	40	ns
t _{rec}	recovery time	nCD to nA, nB; see Fig. 7	5 V		-	20	40	ns
			10 V		-	10	20	ns
			15 V		-	5	10	ns
t _{rtrig}	retrigger time	nQ , $n\overline{Q}$ to $n\overline{A}$, nB ;	5 V		0	-	-	ns
		see Fig. 7	10 V		0	-	-	ns
			15 V		0	-	-	ns

Dual precision monostable multivibrator

Symbol	Parameter	Conditions	V_{DD}	Extrapolation formula[1]	Min	Тур	Max	Unit
t _W	Symbol Parameter pulse width	nA LOW; minimum width;	5 V		90	45	-	ns
		see Fig. 7	10 V		30	15	-	ns
			15 V		24	12	-	ns
		nB HIGH;minimum width;	5 V		50	25	-	ns
		see Fig. 7	10 V		24	12	-	ns
			15 V		20	10	-	ns
		nCD LOW; minimum width; see Fig. 7	5 V		55	25	-	ns
			10 V		25	12	-	ns
			15 V		20	10	-	ns
		nQ or n \overline{Q} ; R _{EXT} = 100 kΩ; C _{EXT} =2.0 nF; see <u>Fig. 7</u>	5 V		218	230	242	μs
			10 V		213	224	235	μs
			15 V		211	223	234	μs
		nQ or $n\overline{Q}$; R_{EXT} = 100 kΩ; C_{EXT} = 0.1 μF; see Fig. 7	5 V		10.3	10.8	11.3	ms
			10 V		10.2	10.7	11.2	ms
			15 V		10.1	10.6	11.1	ms
		\overline{nQ} or \overline{nQ} ; R_{EXT} = 100 kΩ;	5 V		1.01	1.09	1.11	s
		$C_{EXT} = 10 \mu F$; see Fig. 7	10 V		0.99	1.04	1.09	s
			15 V		0.99	1.04	1.09	s
Δt_W	pulse width	nQ or nQ variation over	5 V		-	±0.2	-	%
	variation	temperature range; see Fig. 8	10 V		-	±0.2	-	%
		300 <u>1 lg. 0</u>	15 V		-	±0.2	-	%
		nQ or nQ variation over V _{DD} voltage range 5 V to 15 V; see Fig. 9			-	±1.5	-	%
		nQ or nQ variation	5 V		-	±1	-	%
		between monostables in the same device;	10 V		-	±1	-	%
		R_{EXT} = 100 kΩ; C_{EXT} = 2 nF to 10 μF	15 V		-	±1	-	%
R _{EXT}	external timing resistor				5	-	[3]	kΩ
C _{EXT}	external timing capacitor				2000	-	no Iimits	pF

The typical values of the propagation delay and transition times are calculated from the extrapolation formulas shown (C_L in pF).

 t_t is the same as t_{THL} and t_{TLH} . The maximum permissible resistance R_{EXT} , which holds the specified accuracy of t_W (nQ, n \overline{Q} output), depends on the leakage current of the capacitor C_{EXT} and the leakage current of the HEF4538B.

Dual precision monostable multivibrator

10.1. Waveforms and test circuit

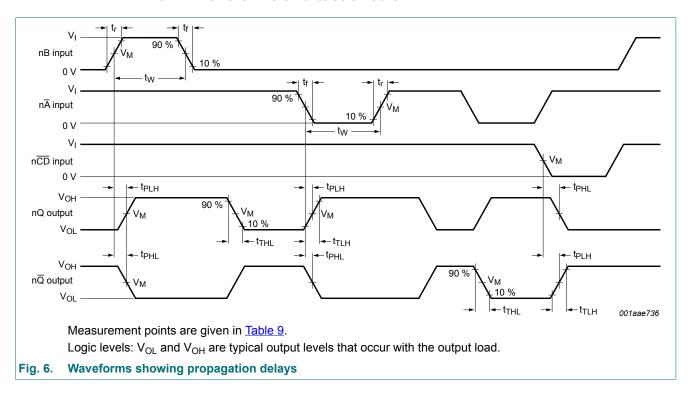


Table 9. Measurement points

Supply voltage	Input	Output
V_{DD}	V _M	V _M
5 V to 15 V	0.5V _{DD}	0.5V _{DD}

Dual precision monostable multivibrator

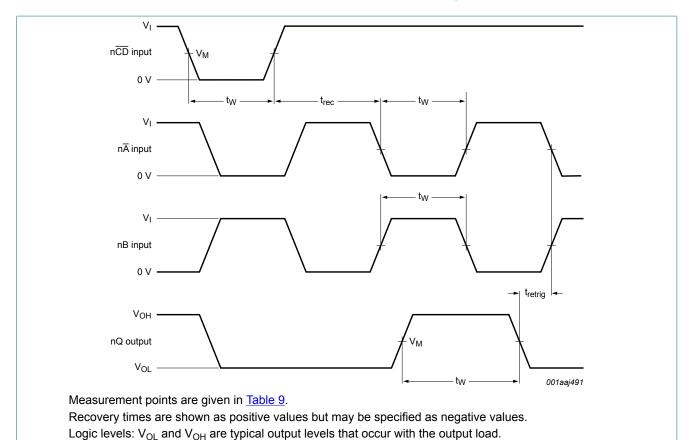
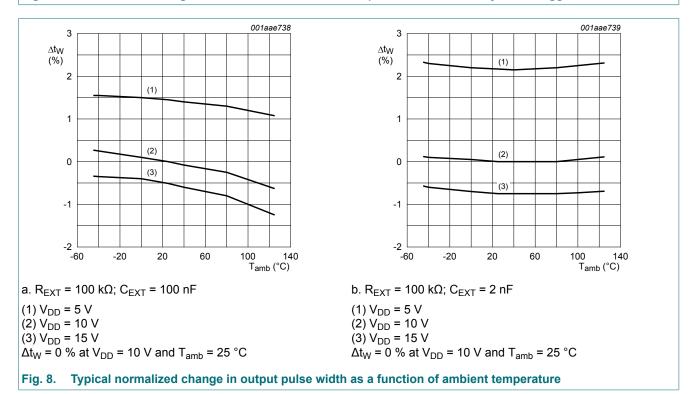
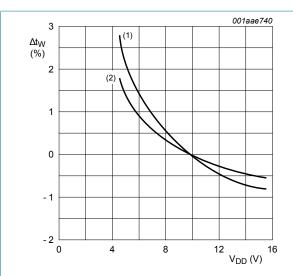


Fig. 7. Waveforms showing minimum nCD, nA, nB, and nQ pulse widths, recovery and retrigger times



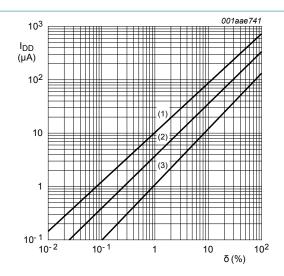
Dual precision monostable multivibrator



 T_{amb} = 25 °C; Δt_W = 0 % at V_{DD} = 10 V; R_{EXT} = 100 k Ω

(1) $C_{EXT} = 2 nF$

(2) $C_{EXT} = 100 \text{ nF}$



 R_{EXT} = 100 k Ω ; C_{EXT} = 100 nF; C_L = 50 pF; one monostable multivibrator switching only

 $(1) V_{DD} = 15 V$

(2) $V_{DD} = 10 \text{ V}$

(3) $V_{DD} = 5 V$

Typical normalized change in output pulse width as a function of the supply voltage

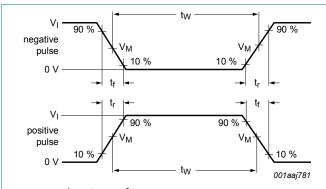
Fig. 10. Total supply current as a function of the output duty factor

 V_{DD}

b. Test circuit

V٥

001aag182



a. Input waveforms

Test data is given in Table 10.

Definitions for test circuit:

DUT = Device Under Test.

 C_L = load capacitance including jig and probe capacitance.

 R_T = termination resistance should be equal to the output impedance Z_0 of the pulse generator.

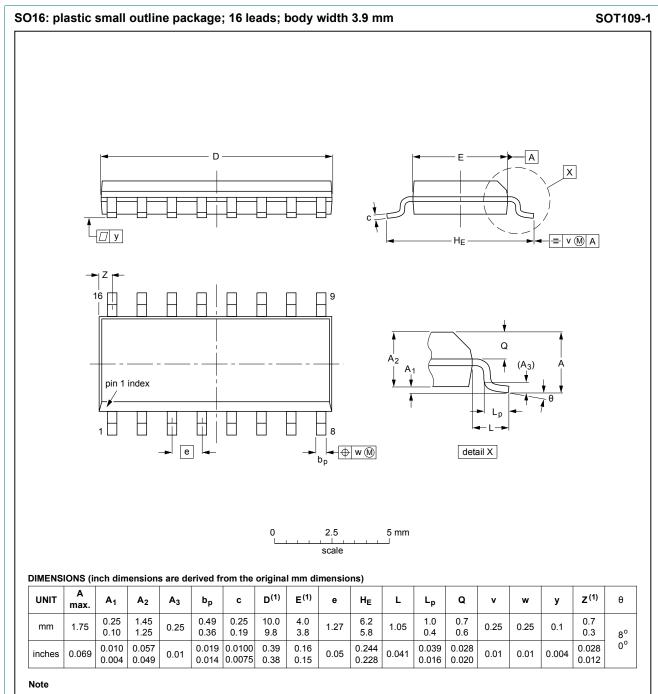
Fig. 11. Test circuit for measuring switching times

Table 10. Test data

Supply voltage	Input	Load	
V_{DD}	VI	CL	
5 V to 15 V	V _{SS} or V _{DD}	≤ 20 ns	50 pF

Dual precision monostable multivibrator

11. Package outline



1. Plastic or metal protrusions of 0.15 mm (0.006 inch) maximum per side are not included.

OUTLINE				EUROPEAN	ISSUE DATE	
VERSION	IEC	JEDEC	JEITA		PROJECTION	ISSUE DATE
SOT109-1	076E07	MS-012				99-12-27 03-02-19

Fig. 12. Package outline SOT109-1 (SO16)

Dual precision monostable multivibrator

12. Abbreviations

Table 11. Abbreviations

Acronym	Description
CMOS	Complementary Metal-Oxide Semiconductor
DUT	Device Under Test

13. Revision history

Table 12. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes
HEF4538B v.11	20181019	Product data sheet	-	HEF4538B v.10
Modifications:	Nexperia.	his data sheet has been redes		. 0
HEF4538B v.10	20160401	Product data sheet	-	HEF4538B v.9
Modifications:	Type number I	HEF4538BP (SOT38-4) remov	ed.	
HEF4538B v.9	20131210	Product data sheet	-	HEF4538B v.8
Modifications:	• Fig. 8 and Fig.	9 updated to show output puls	se width over full tem	nperature range.
HEF4538B v.8	20111116	Product data sheet	-	HEF4538B v.7
HEF4538B v.7	20110217	Product data sheet	-	HEF4538B v.6
HEF4538B v.6	20091102	Product data sheet	-	HEF4538B v.5
HEF4538B v.5	20090304	Product data sheet	-	HEF4538B v.4
HEF4538B v.4	20090206	Product data sheet	-	HEF4538B_CNV v.3
HEF4538B_CNV v.3	19950101	Product specification	-	HEF4538B_CNV v.2
HEF4538B_CNV v.2	19950101	Product specification	-	-

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Data sheet status

14. Legal information

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- Please consult the most recently issued document before initiating or completing a design.
- [2] The term 'short data sheet' is explained in section "Definitions".
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Dual precision monostable multivibrator

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Dual precision monostable multivibrator

Contents

1.	General description	1
2.	Features and benefits	1
3.	Ordering information	. 1
4.	Functional diagram	.2
5.	Pinning information	3
5.1	. Pinning	. 3
5.2	Pin description	3
6.	Functional description	3
7.	Limiting values	4
8.	Recommended operating conditions	. 5
	Recommended operating conditions Static characteristics	
9.		.5
9. 10.	Static characteristics	.5
9. 10 . 10.	Static characteristics Dynamic characteristics 1. Waveforms and test circuit	. 5 6
9. 10. 10. 11.	Static characteristics Dynamic characteristics	.5 6 8
9. 10. 10. 11. 12.	Static characteristics	.5 6 8 11
9. 10. 10. 11. 12.	Static characteristics	.5 8 11

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